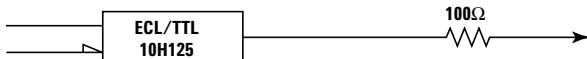


## Data Pod Characteristics

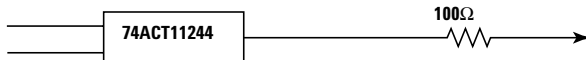
### 10461A TTL Data Pod

<b>Output type</b>	10H125 with 100 $\Omega$ series
<b>Maximum clock</b>	200 MHz
<b>Skew (note 1)</b>	typical < 2 ns; worst case = 4 ns
<b>Recommended lead set</b>	Agilent 10474A



### 10462A 3-STATE TTL/CMOS Data Pod

<b>Output type (note 2)</b>	74ACT11244 with 100 $\Omega$ series; 10H125 on non 3-state channel 7
<b>3-State enable</b>	negative true, 100 K $\Omega$ to GND, enabled on no connect
<b>Maximum clock</b>	100 MHz
<b>Skew (note 1)</b>	typical < 4 ns; worst case = 12 ns
<b>Recommended lead set</b>	Agilent 10474A



Note 1: Typical skew measurements made at pod connector with approximately 10 pF/50 k $\Omega$  load to GND; worst case skew numbers are a calculation of worst case conditions through circuits.

Note 2: Channel 7 on the 3-state pods has been brought out in parallel as a non 3-state signal. By looping this output back into the 3-state enable line, the channel can be used as a 3-state enable.