

Table 13-3 HP 44724A Specifications

Maximum Input Voltage:	55 V DC Between High and Low Terminal (of each channel) 350 V peak or 250 V DC (between channels or between any Terminal and Chassis)
Maximum Sink Current:	500 mA DC per channel (1 A fuse protected)
Maximum Reverse Polarity Current:	500 mA DC per channel
TTL Compatibility:	200 mA per channel with $V_{out} \leq 0.4$ volts
Closed Channel Path Resistance:	1.5 Ω
Maximum Off Leakage:	0.25 mA per channel at 55 V DC
Maximum Wire Size:	16 AWG